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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta, et al.      Art Unit : Unknown  
Serial No. : 09/387,109      Examiner : Unknown  
Filed : August 31, 1999  
Title : SDRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents  
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INFORMATION DISCLOSURE STATEMENT

Copies of the references listed on the attached form PTO-1449 are enclosed. A copy of a communication from a foreign patent office in a counterpart application is also enclosed.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: May 12, 2003

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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